



REPLACEMENT SHEET

ASSEMBLY JIG AND MANUFACTURING METHOD OF MULTILAYER SEMICONDUCTOR DEVICE

Inventor: Yoshiyuki Yanagisawa et al.

Serial No. 09/876,290

Robert J. Depke, Holland & Knight LLP - (312) 263-3600

FIG.2(a)

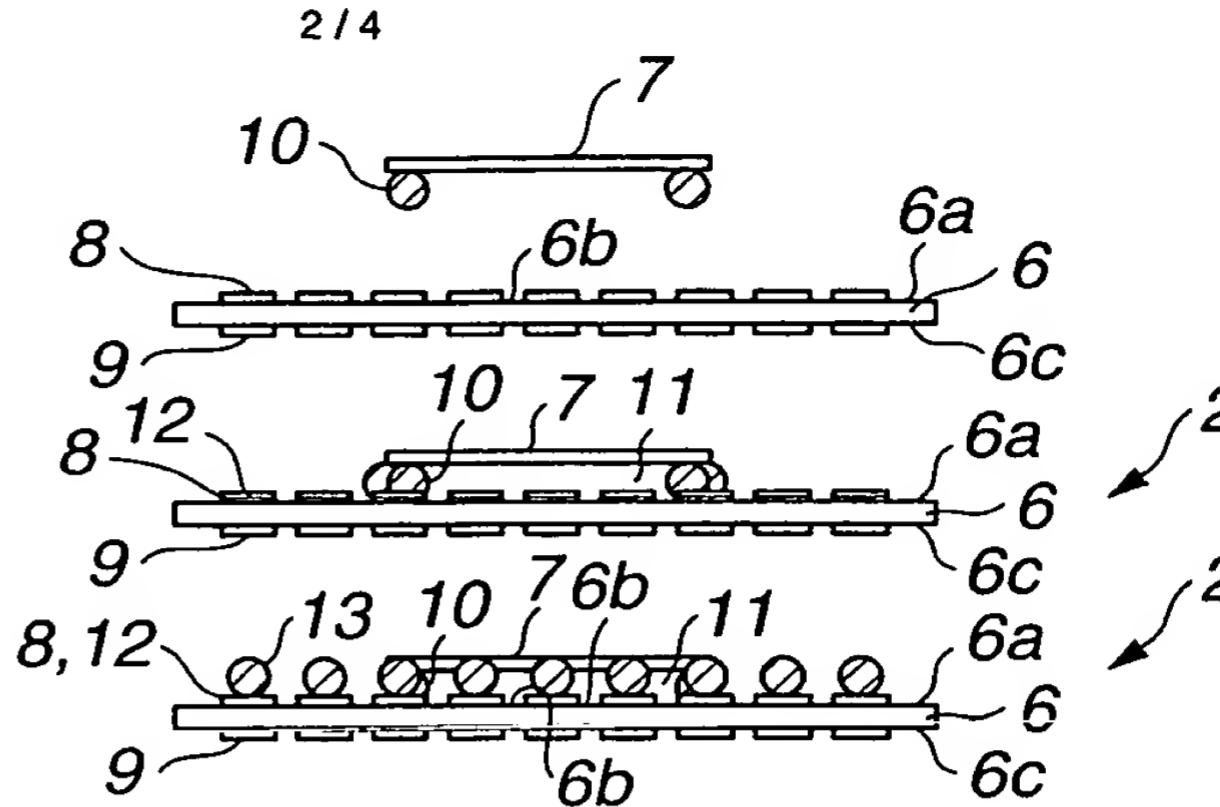


FIG.2(b)

FIG.2(c)

FIG.2(d)

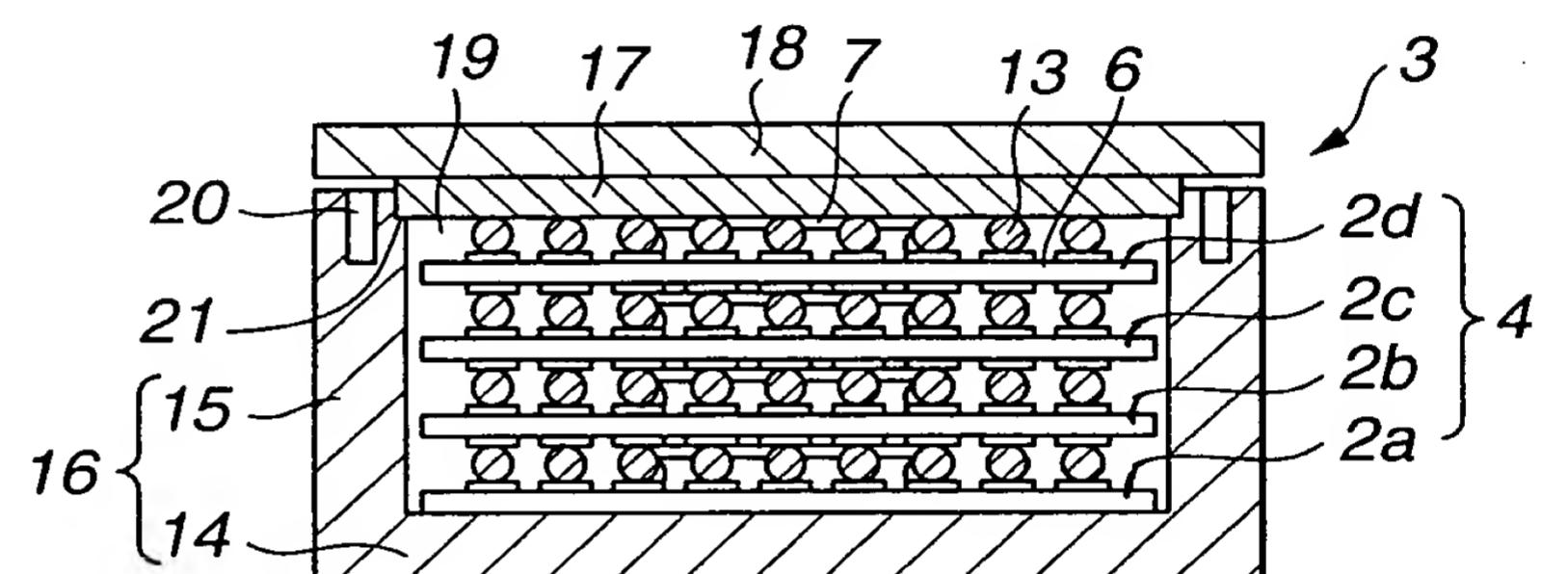


FIG.2(e)

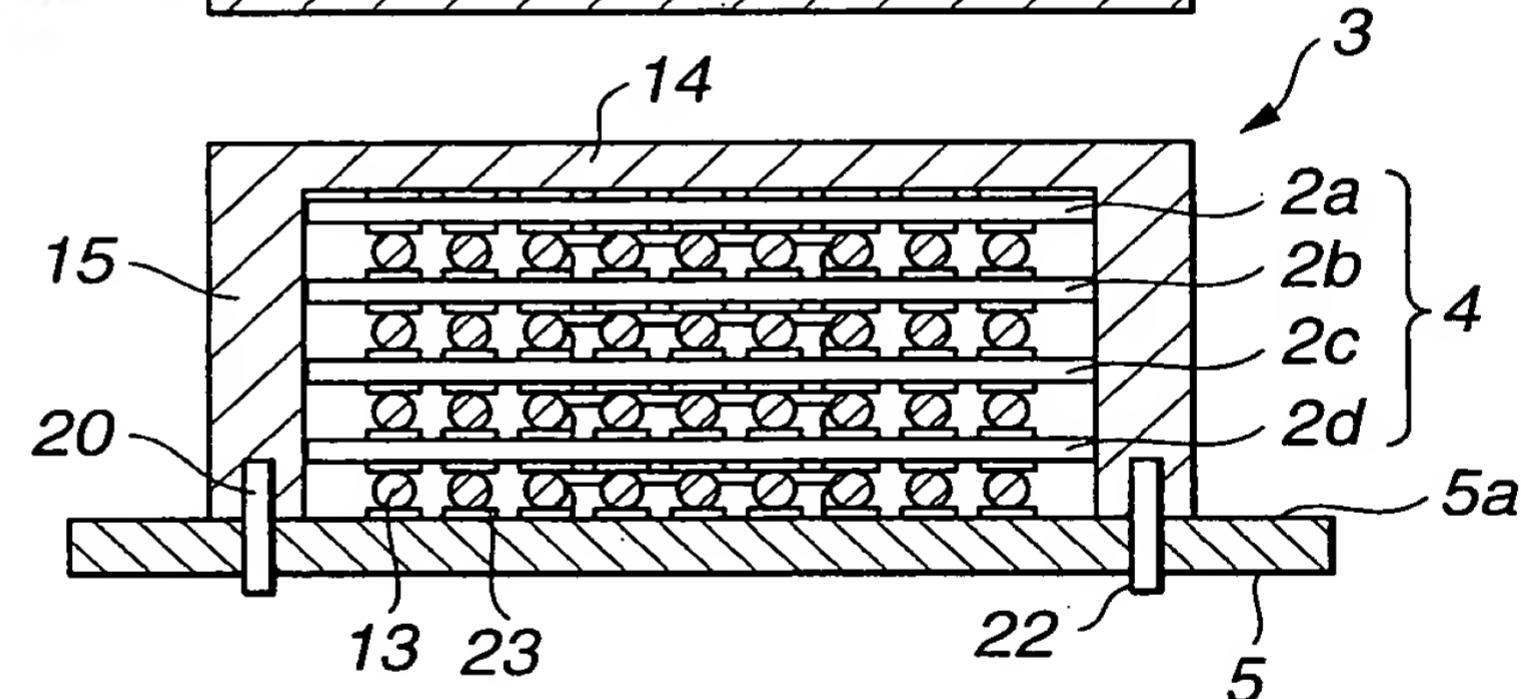
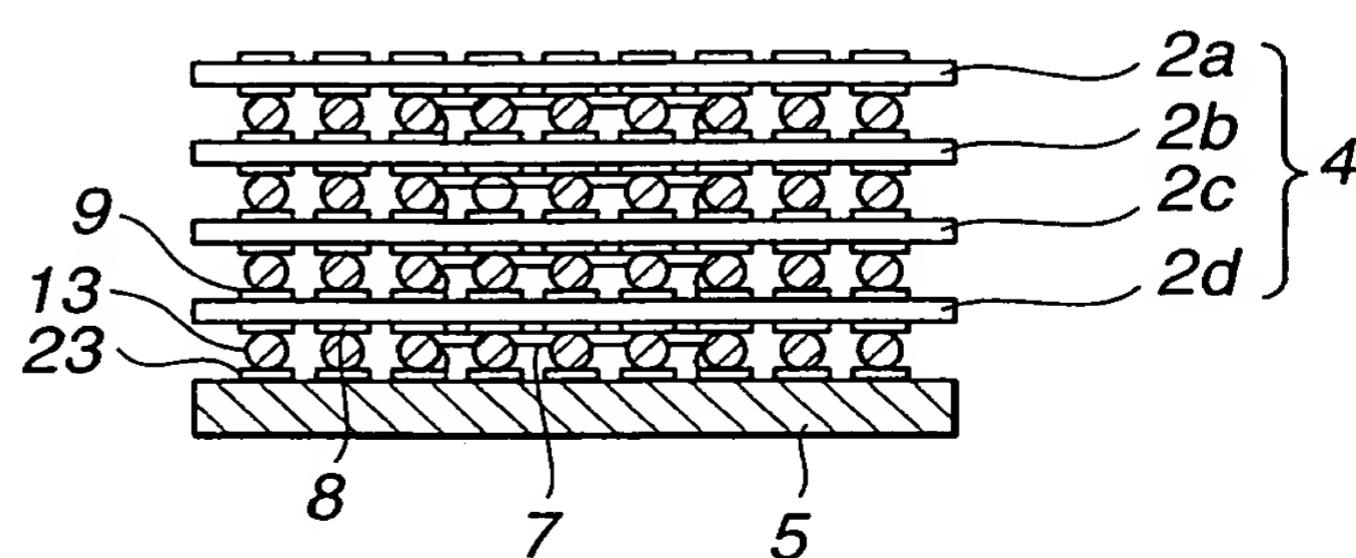


FIG.2 (f)





REPLACEMENT SHEET

ASSEMBLY JIG AND MANUFACTURING METHOD OF MULTILAYER SEMICONDUCTOR DEVICE

Inventor: Yoshiyuki Yanagisawa et al.

Serial No. 09/876,290

Robert Depke, Holland & Knight LLP - (312) 223-3600

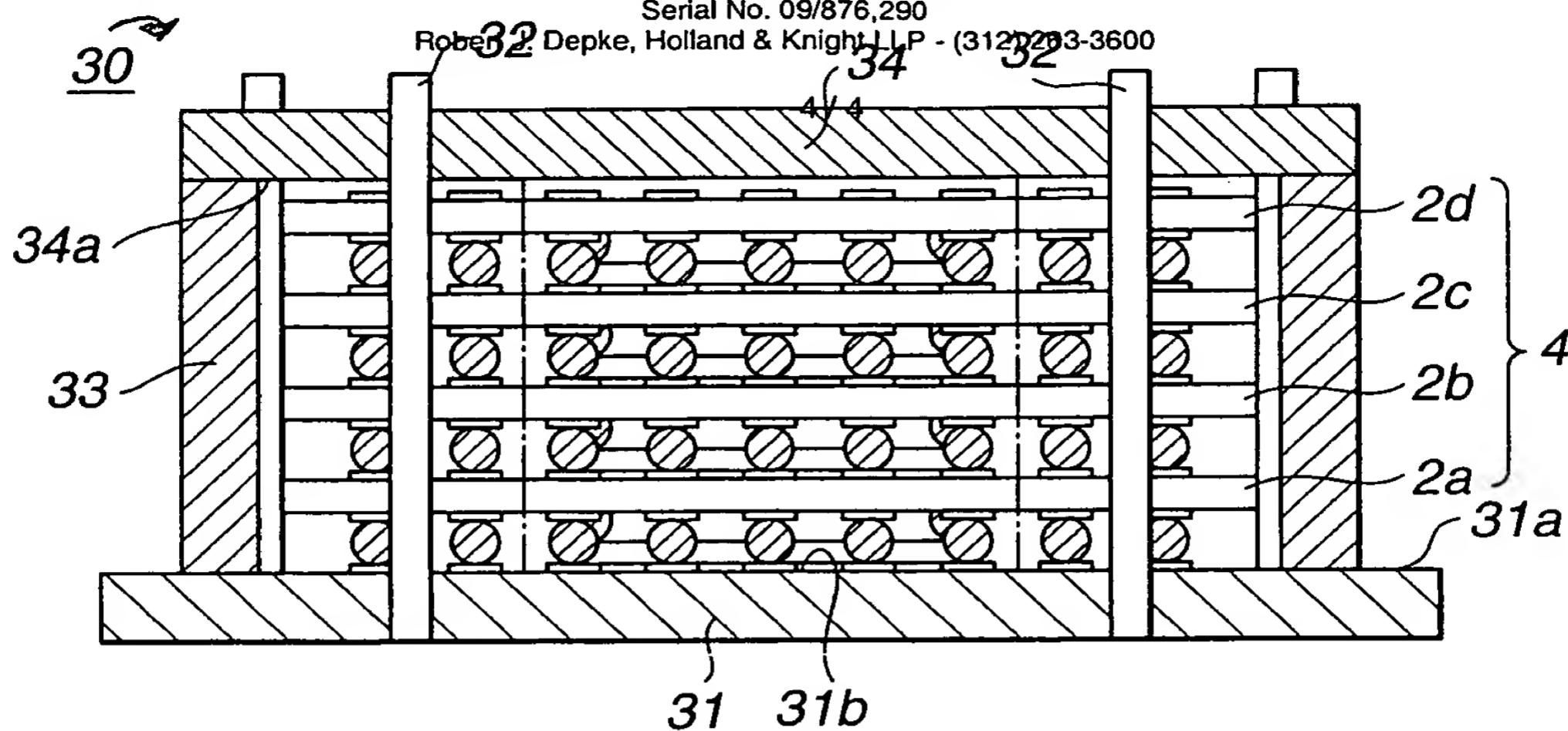


FIG.5(a)

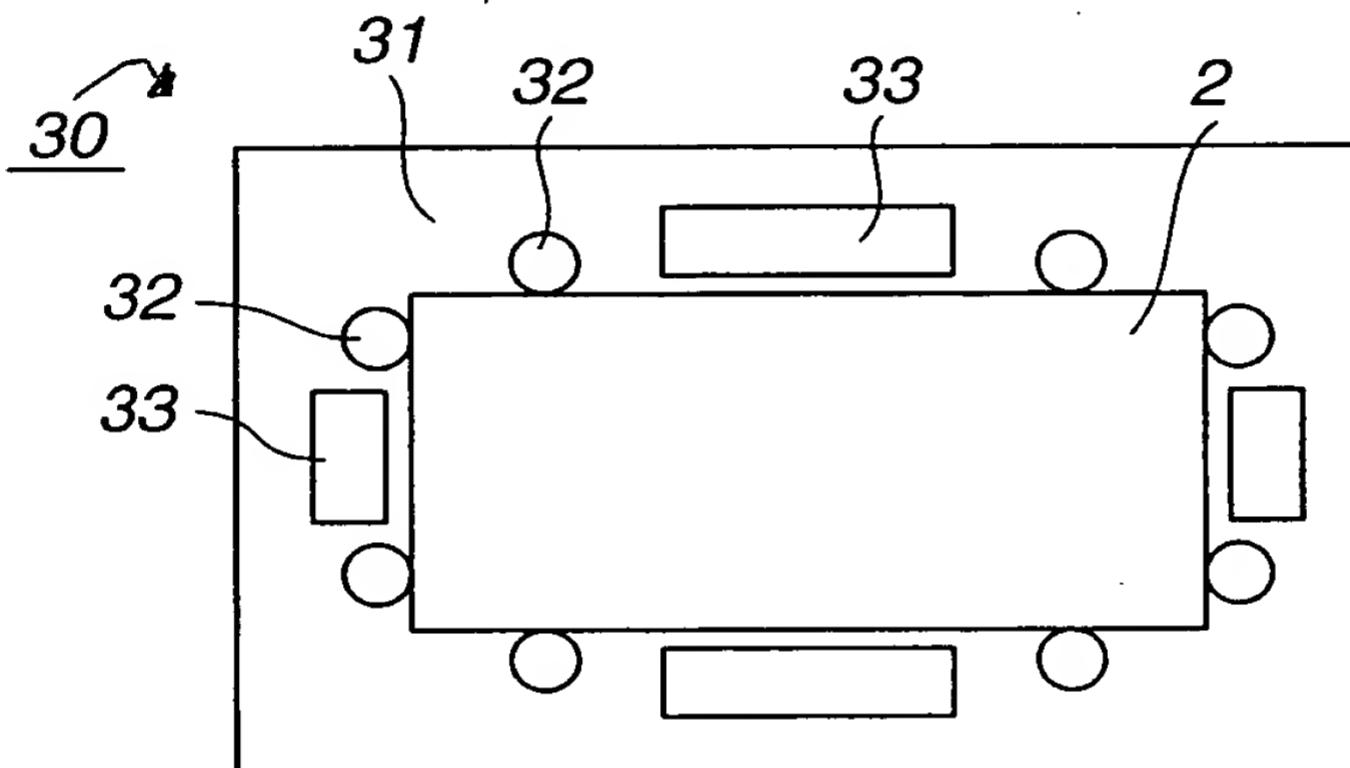


FIG.5(b)

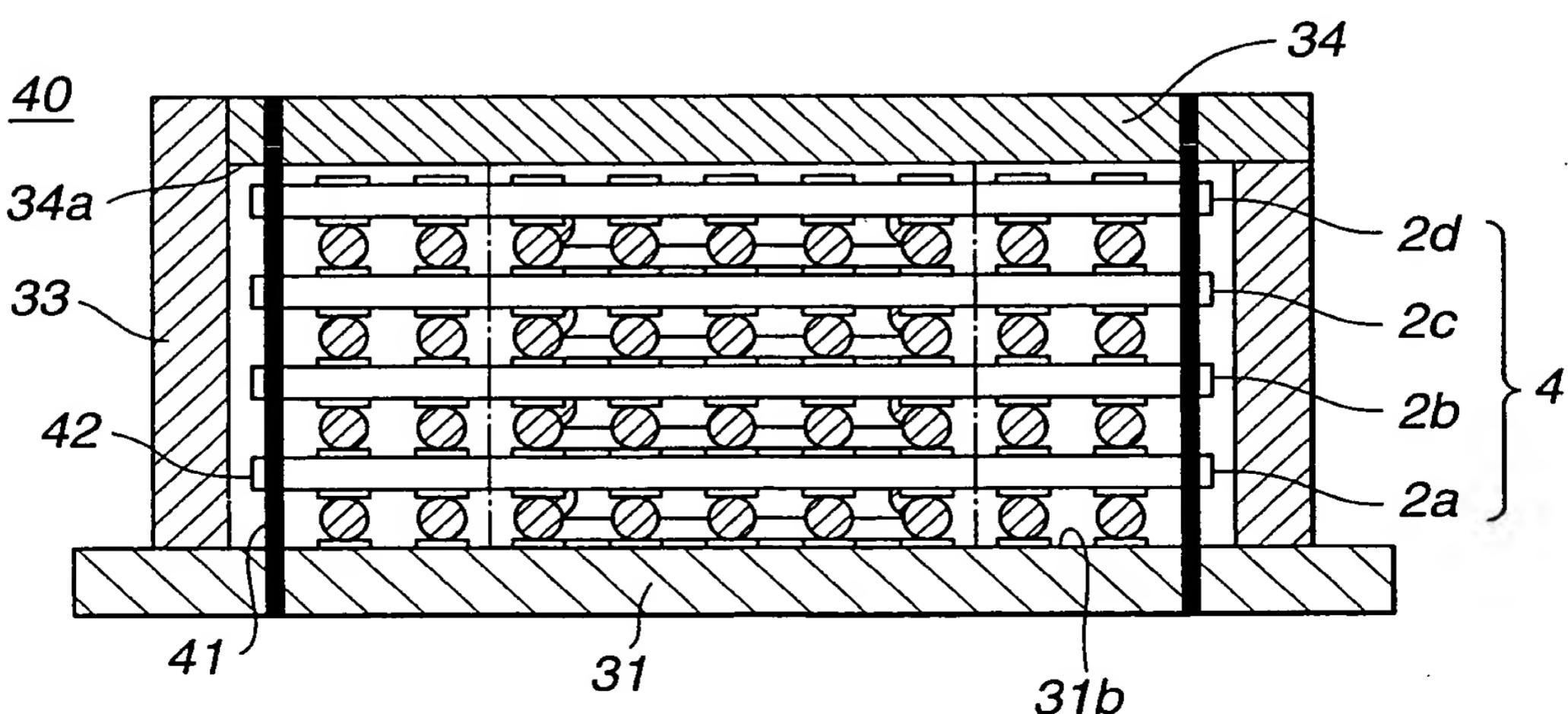


FIG.6